

Amendments to and Listing of the Claims:

This listing of claims replaces all prior versions, and listings, of claims in this application.

Listing of Claims:

1 – 3. (Canceled)

4. (Currently Amended) An interface device coupled to a board on which integrated circuits are mounted for providing electrostatic discharge protection for the integrated circuits comprising:

a plurality of first contact members of a first length, each of the first contact members including one end connected to the board and the other end to connect to an external device; and

at least two second contact members of a second length, each of the second contact members being connected to a voltage line of a voltage level, wherein one of the at least two second contact members include (i) one contact member connected to a first voltage line of a first voltage level, and (ii) another contact member connected to a second voltage line of a second voltage level smaller than the first voltage level;

wherein the second length is greater than the first length such that when the board is coupled to the external device through the interface device in a direction, the second contact members contact the external device earlier than the first contact members.

5 – 10. (Canceled)

11. (Currently Amended) An interface device formed on a board on which integrated circuits are mounted for providing electrostatic discharge protection for the integrated circuits, comprising:

a plurality of first contact lines of a first length, each of the first contact lines including one end connected to the board and the other end to connect to an external device, the one ends of the first contact lines being aligned to an aligning line;

at least one second contact line of a second length corresponding to at least one voltage line of a first voltage level to which the integrated circuits are connected, each of the at least one

second contact line being connected to a corresponding voltage line at one end aligned with the aligning line; and

a third contact line directly connected to a second voltage level including a third length measured from the aligning line to one end of the third contact line;

wherein the second length and the third length are greater than the first length and the third length is equal to the second length such that when the board is coupled to the external device, electric charges accumulated on the board are discharged via at least one of the third contact line or second contact line.

12 – 27. (Canceled)

28. (Currently Amended) A detecting system for detecting integrated circuits formed on a board, comprising:

a test device including a first board;
a plurality of first pins formed on the first board;
a second board including a first surface and a second surface;
a plurality of first contact points formed on the first surface of the second board to receive the first pins;
a plurality of second pins formed on the second surface of the second board; and
a plurality of second contact points formed on each of the integrated circuits to receive the second pins,

wherein the plurality of first pins includes one or more first pins that are longer in length than the other first pins, or the plurality of second pins includes one or more second pins that are longer in length than the other second pins;

wherein electric charges accumulated on the board on which the integrated circuits are formed are discharged from ~~the longer of the first pins and the second pins~~ one or more of the longer first pins or longer second pins.

29 – 30. (Canceled)

31. (Currently Amended) A method of providing electrostatic discharge protection for integrated circuits formed on a board, comprising:

providing an interface device including:

a plurality of first contact members of a first length, each of the first contact members including one end connected to the board and the other end to connect to an external device; and

at least ~~one~~ two second contact members of a second length connected to a voltage line of a voltage level wherein one of the at least two second contact members include one connected to a first voltage line of a first voltage level, and another connected to a second voltage line of a second voltage level smaller than the first voltage level ;

providing the at least ~~one~~ two second contact members with a length greater than ~~that the~~ length of each of the first contact members;

coupling the board to the external device through the interface device; and

discharging electric charges accumulated on the board via the at least ~~one~~ two second contact members.

32 – 38. (Canceled)

39. (Currently Amended) A method of providing electrostatic protection for integrated circuits formed on a board, comprising:

forming a plurality of first contact lines of a first length on the board;

providing each of the first contact lines with one end connected to the board and the other end to connect to an external device;

aligning the one ends of the first contact lines to an aligning line;

forming at least one second contact line of a second length greater than the first length on the board corresponding to at least one voltage line of a first voltage level to which the integrated circuits are connected;

providing each of the at least one second contact line with one end aligned to the aligning line;

connecting the one end of each of the at least one second contact line to a corresponding

voltage line;

forming a third contact line directly connected to a second voltage level;

providing the third contact line with a third length measured from the aligning line to one end of the third contact line, the third length being greater than the first length and the third length being equal to the second length;

coupling the board to the external device; and

discharging electric charges accumulated on the board via at least one of the third contact line or second contact line.

40 – 43. (Canceled)

44. (Original) A method of providing electrostatic discharge protection in a detecting system for integrated circuits formed on a board, comprising:

providing a test device including a first board;

forming a plurality of first pins on the first board;

providing a second board including a first surface and a second surface;

forming a plurality of first contact points on the first surface of the second board to receive the first pins;

forming a plurality of second pins on the second surface of the second board;

forming a plurality of second contact points on each of the integrated circuits to receive the second pins;

providing at least one of the first pins with a length greater than that of the other first pins, or providing at least one the second pins with a length greater than that of the other second pins;

coupling the first pins to the first contact points and the second pins to the second contact points; and

discharging electric charges accumulated on the board on which the integrated circuits are formed via the at least one first or second pin of greater length.

45 – 47. (Canceled)